

1/13

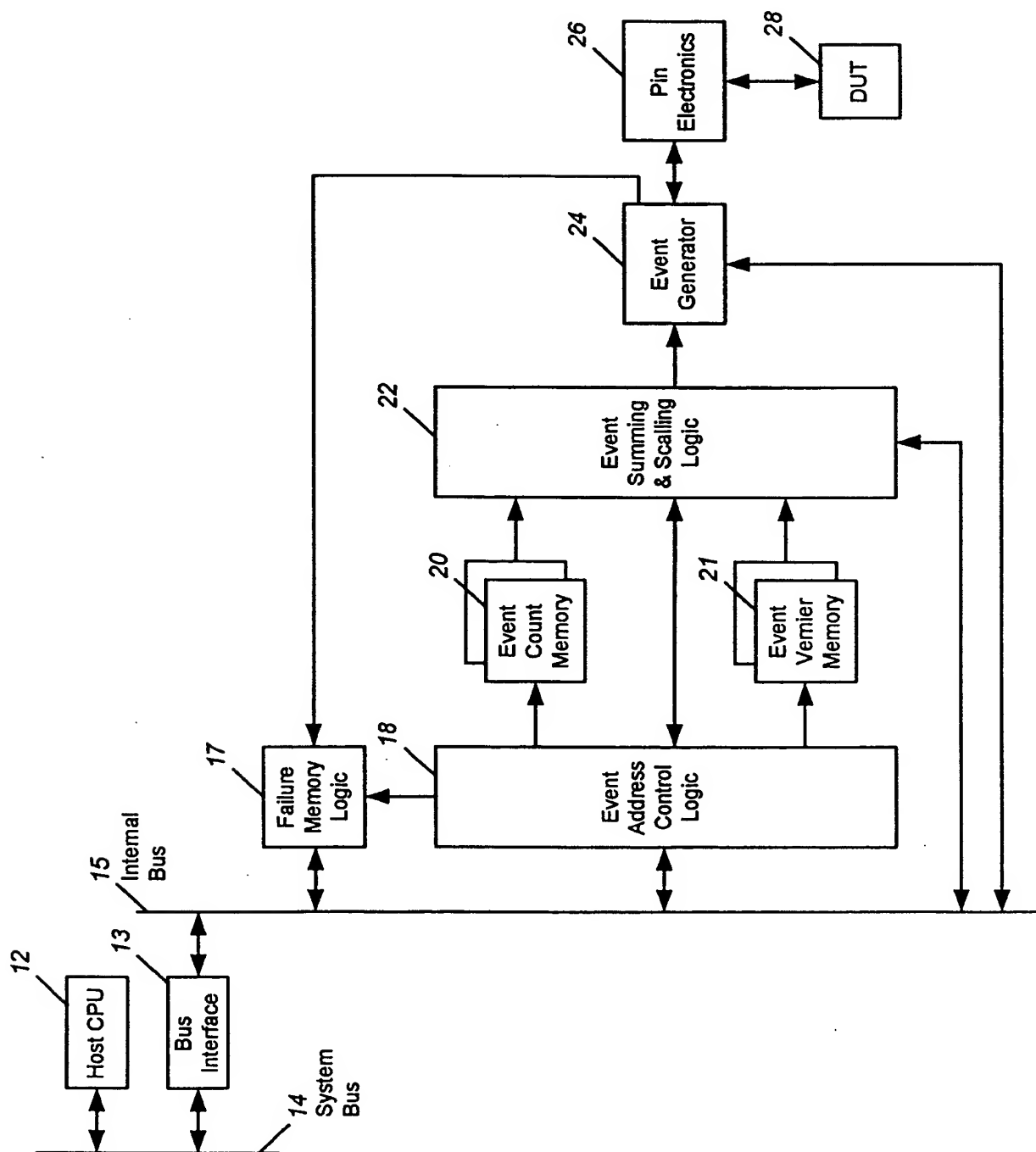


FIG. 1

2/13

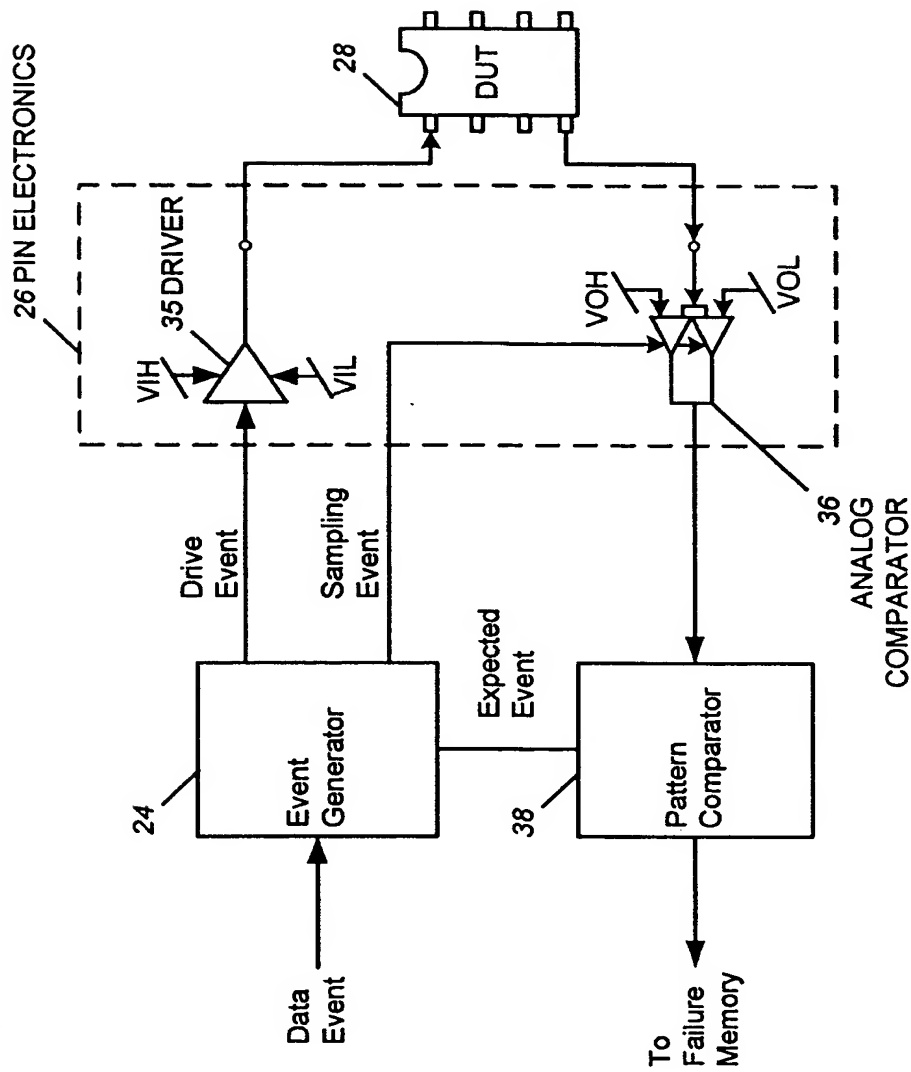


FIG. 2

3/13

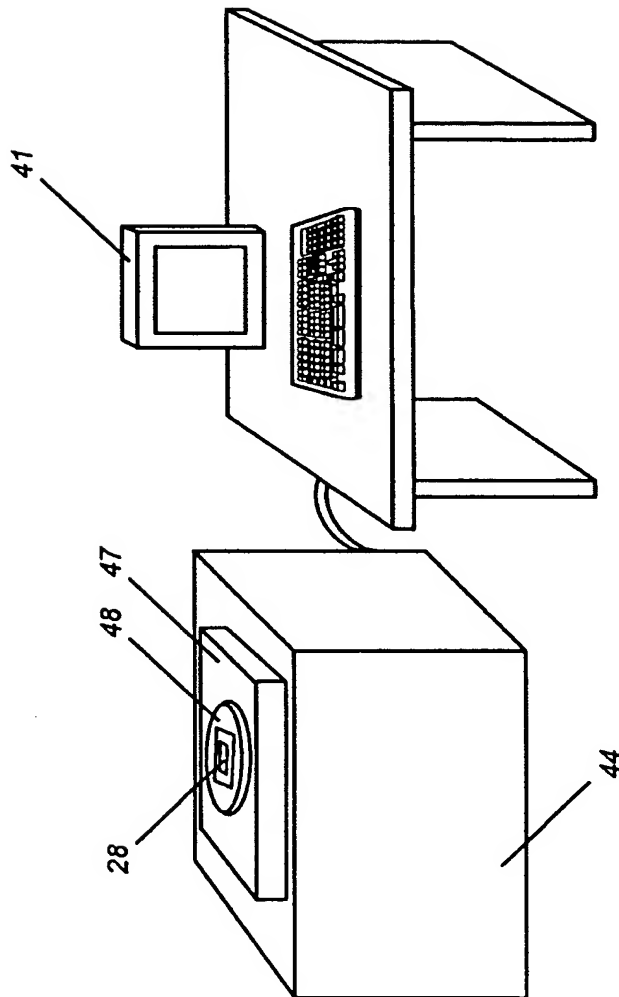


FIG. 3

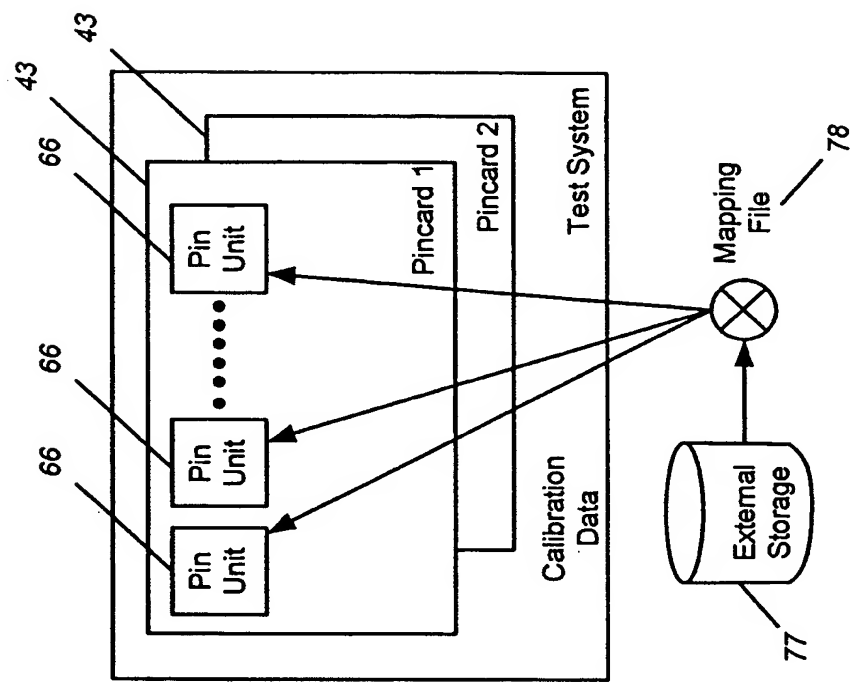


FIG. 5

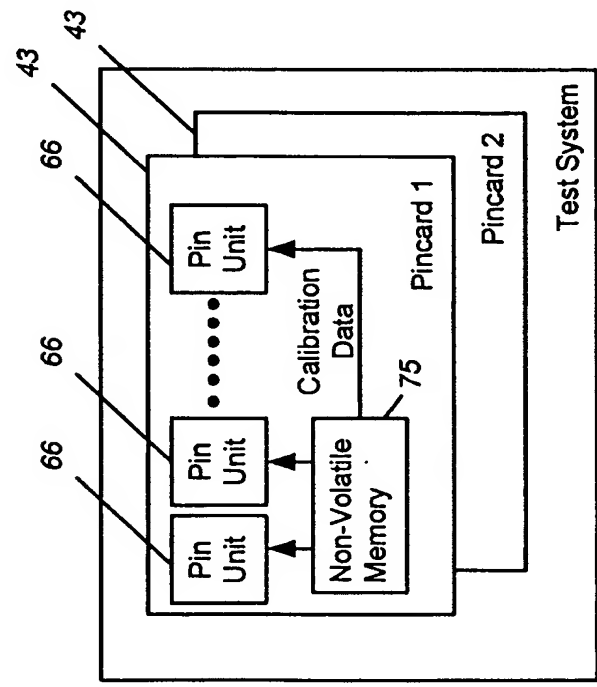


FIG. 4

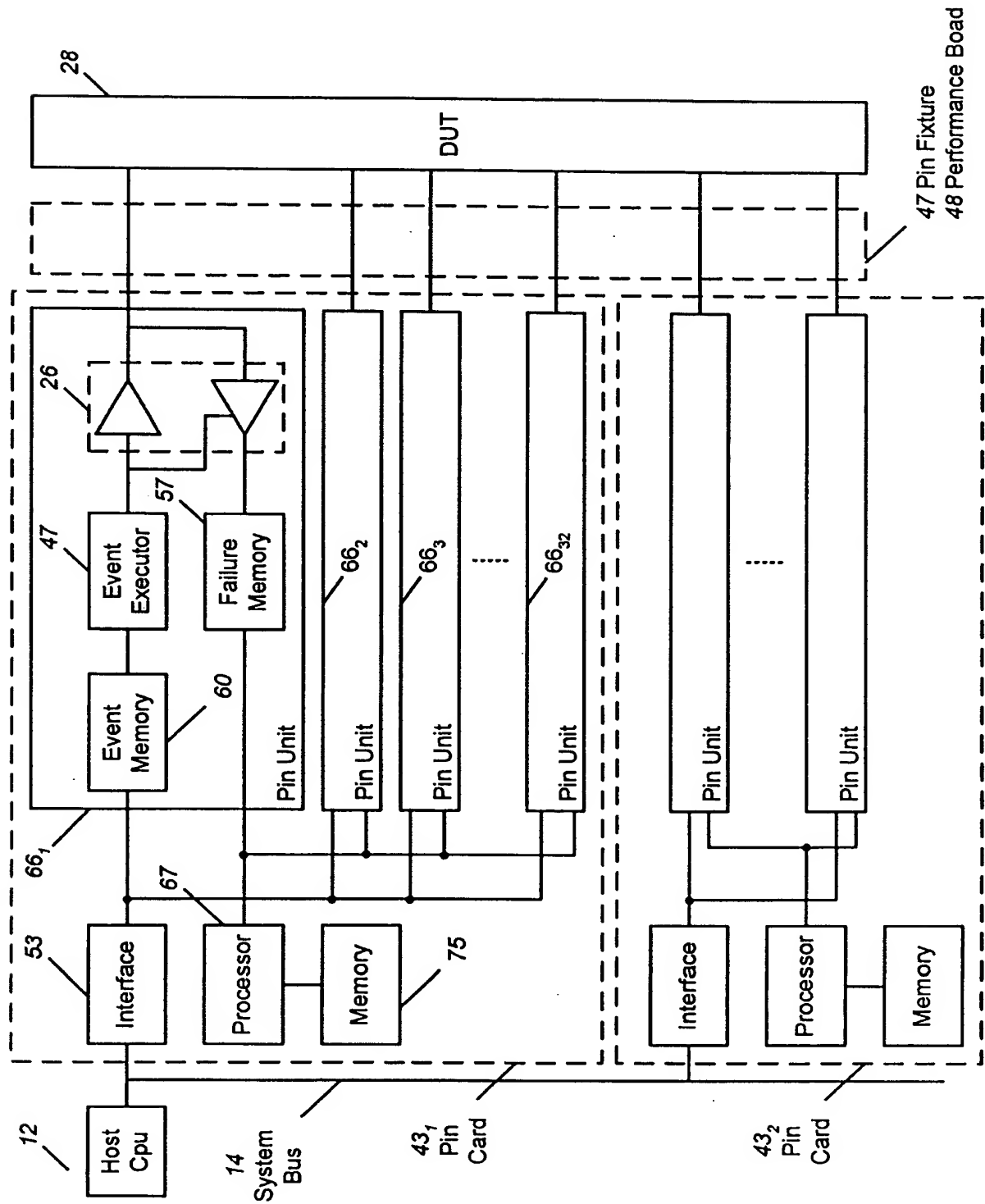


FIG. 6

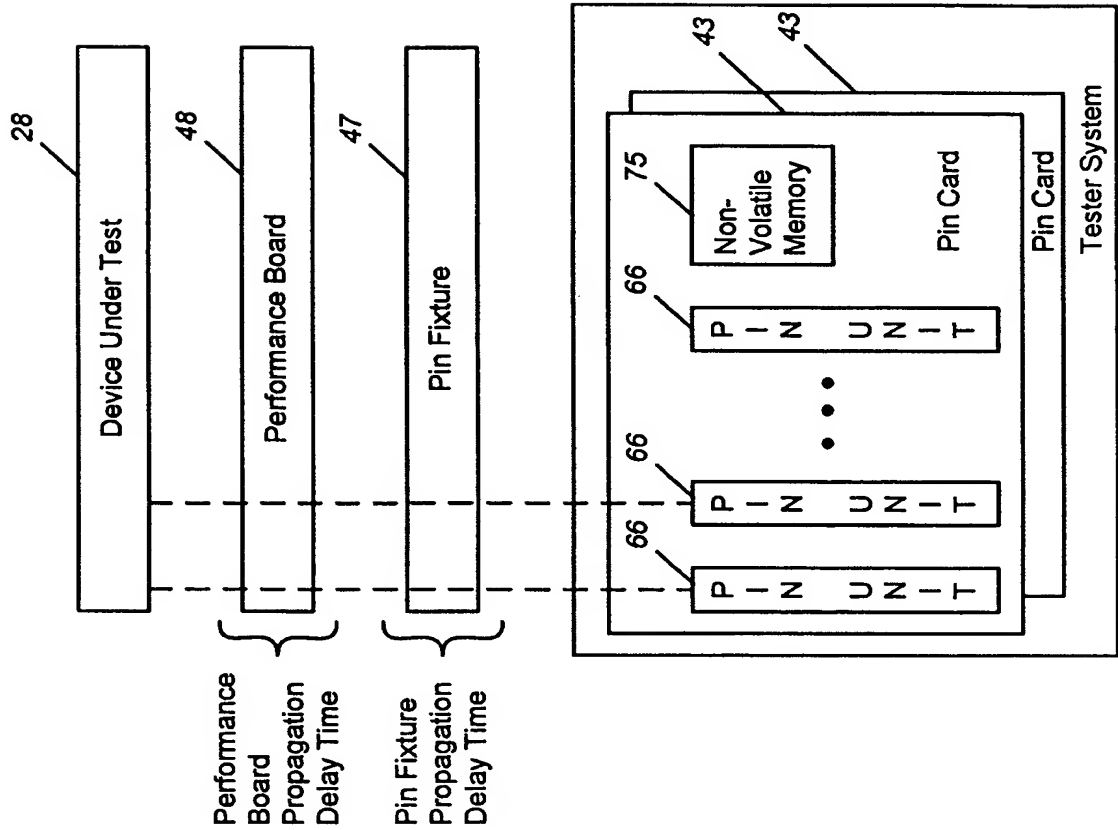


FIG. 7

7/13

```
Calibration_Data_Configure()
{
    struct PIN_UNIT {
        unsigned int corcv; /* correction offset to reference comparison voltage register */
        unsigned int cordv; /* correction offset to reference driving voltage register */
    } *pin_unit;
    struct CALIBRATION_MEMORY {
        /* calibration data for correction offset to reference comparison voltage */
        unsigned int corcv;

        /* calibration data for correction offset to reference comparison voltage */
        unsigned int corcv;
    } *calibration_memory;

    /* Base address of calibration memory data */
    #define CALIBRATION_MEMORY_BASE
    ((struct CALIBRATION_MEMORY *) 0x100)

    /* Base address of pin units calibration registers */
    #define PIN_UNIT_BASE ((struct PIN_UNIT *) 0x200)
    int unit_count;
    pin_unit = PIN_UNIT_BASE;
    calibration_memory = CALIBRATION_MEMORY_BASE;
    for(unit_count=0 ; unit_count <8; unit_count++)
    {
        pin_unit[unit_count].corcv = calibration_memory[unit_count].corcv;
        pin_unit[unit_count].cordv = calibration_memory[unit_count].cordv;
    }
}
```

FIG. 8

8/13

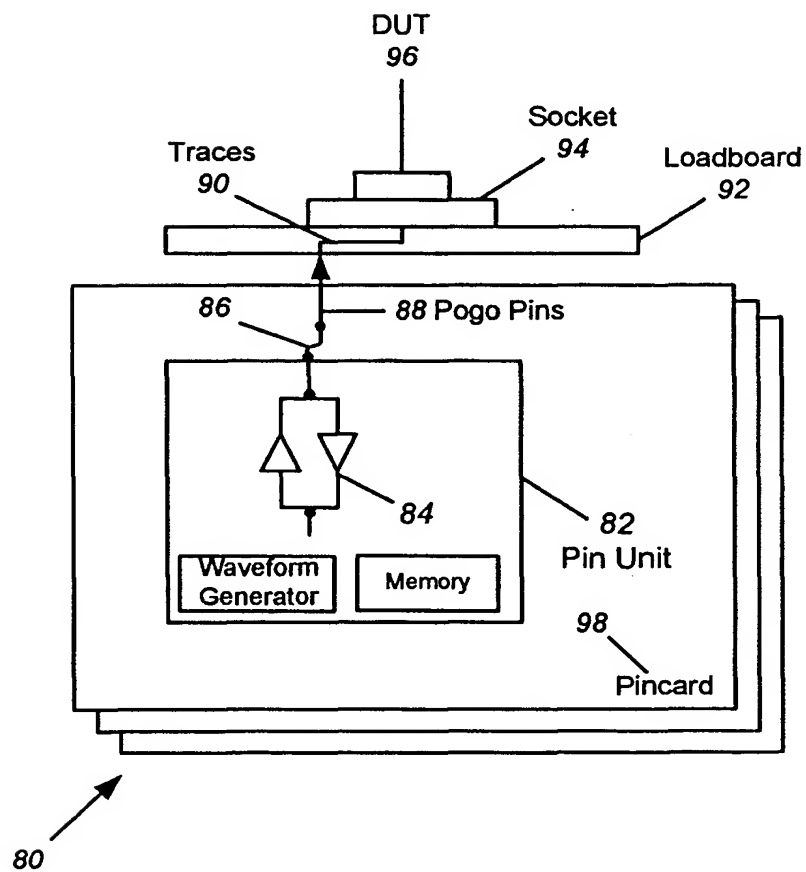
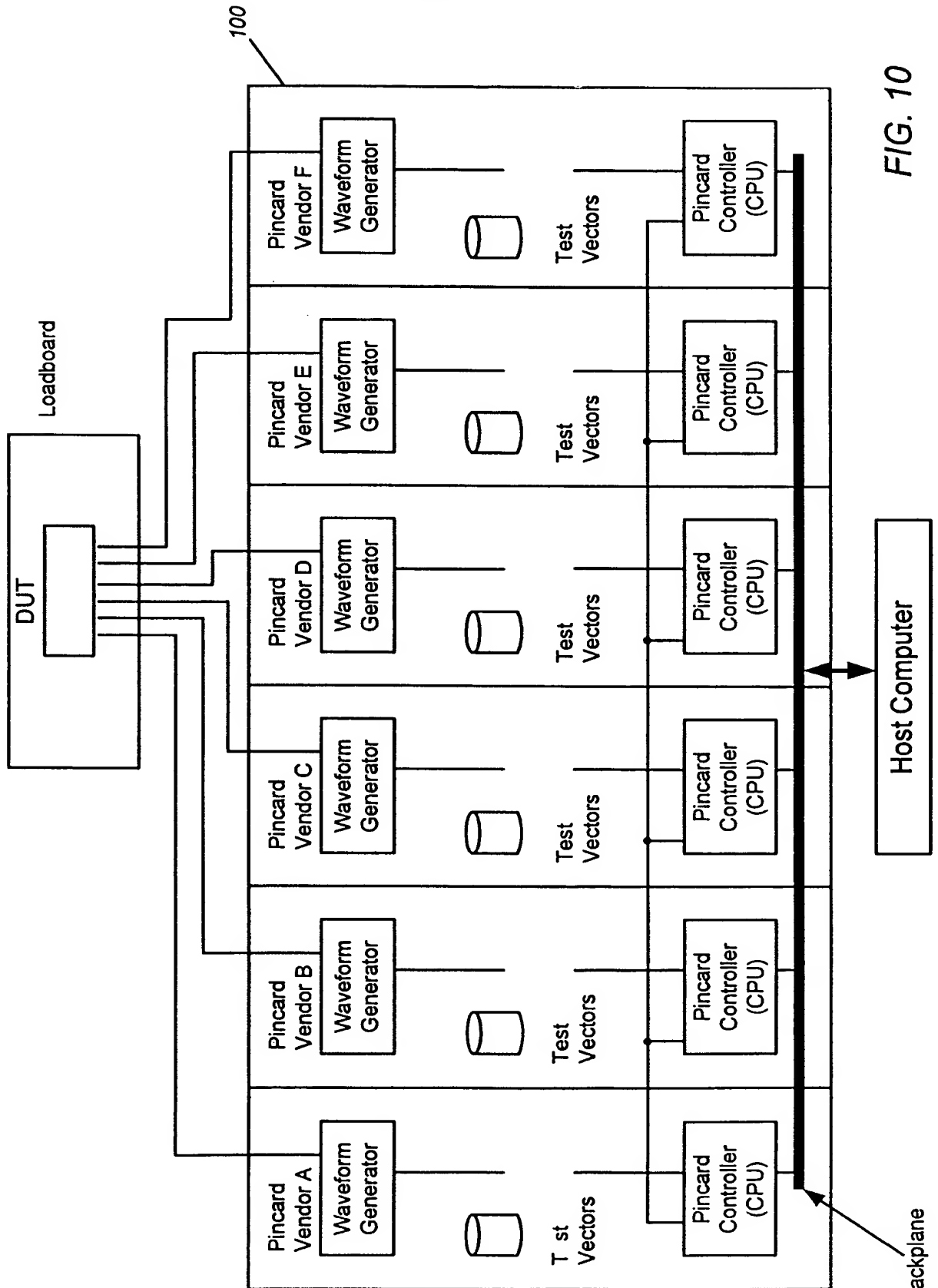
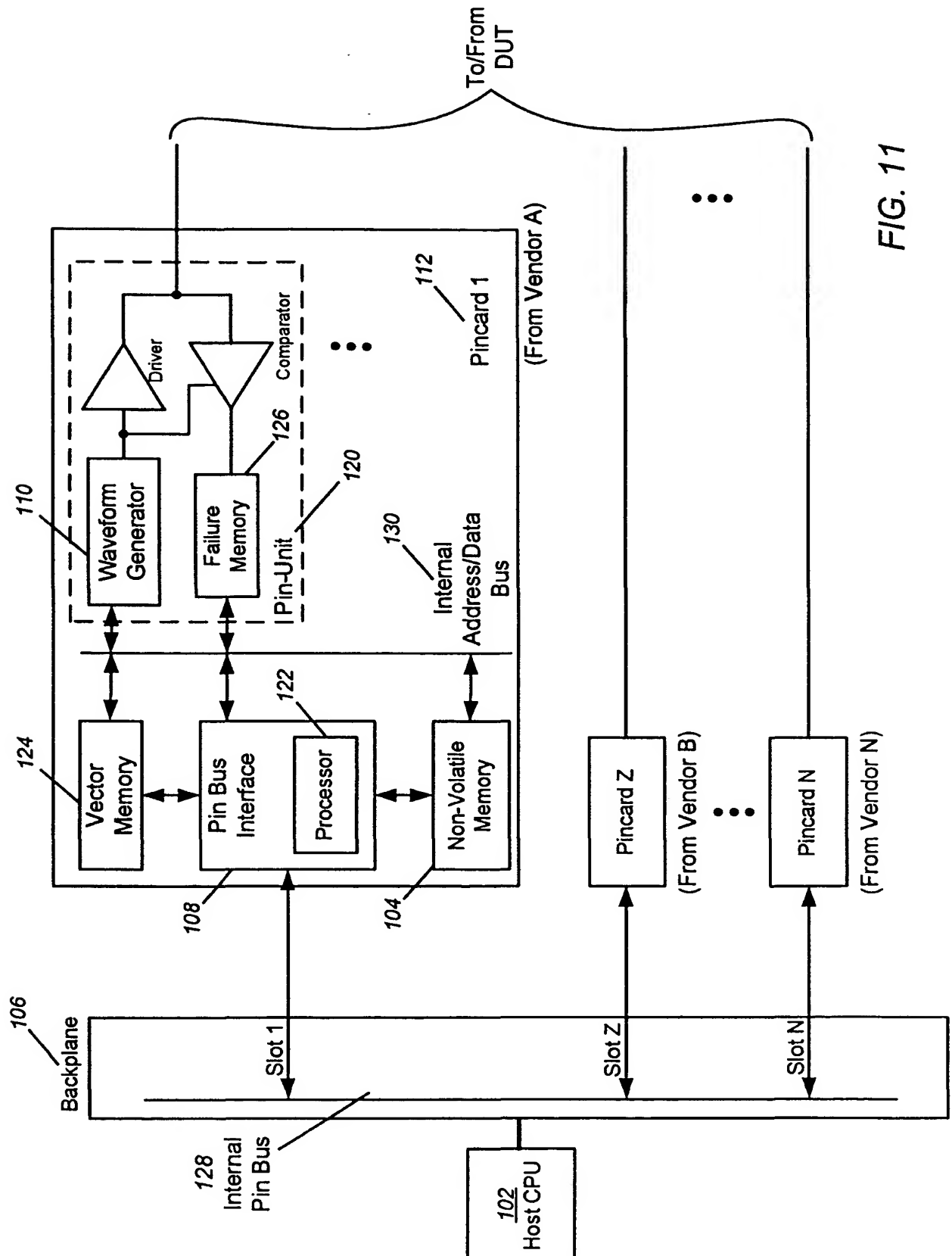


FIG. 9

9/13





11/13

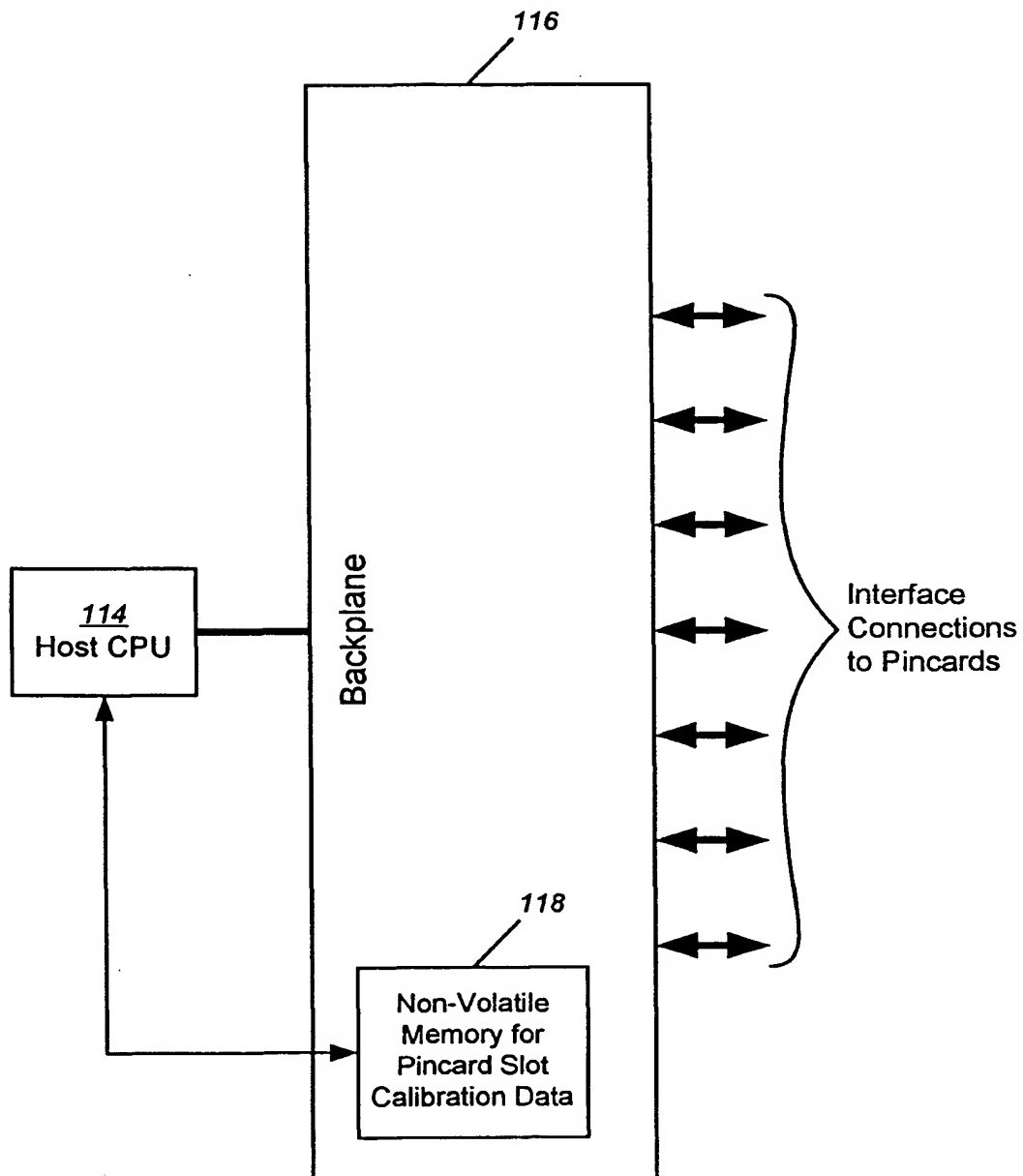


FIG. 12

12/13

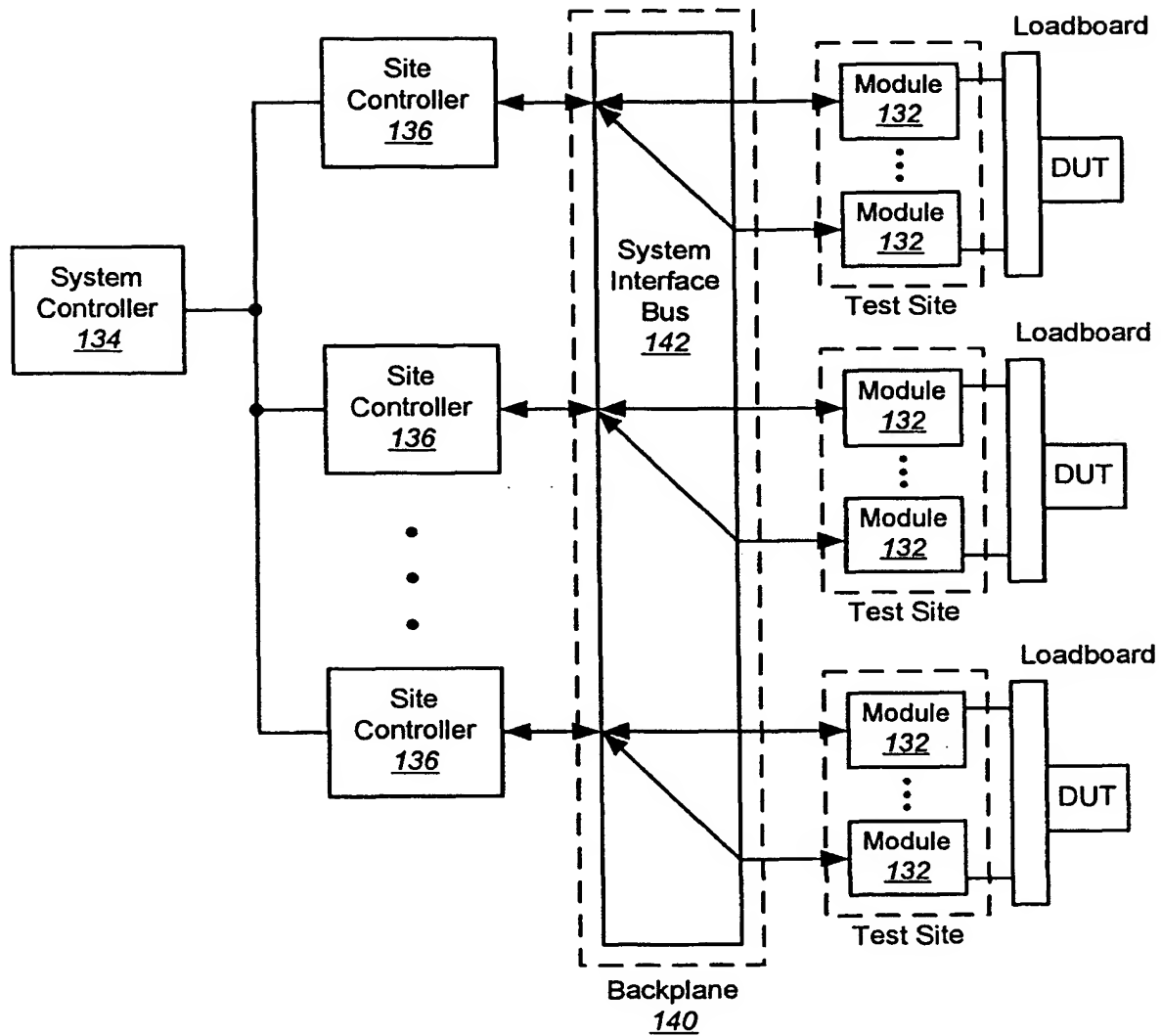


FIG. 13

13/13

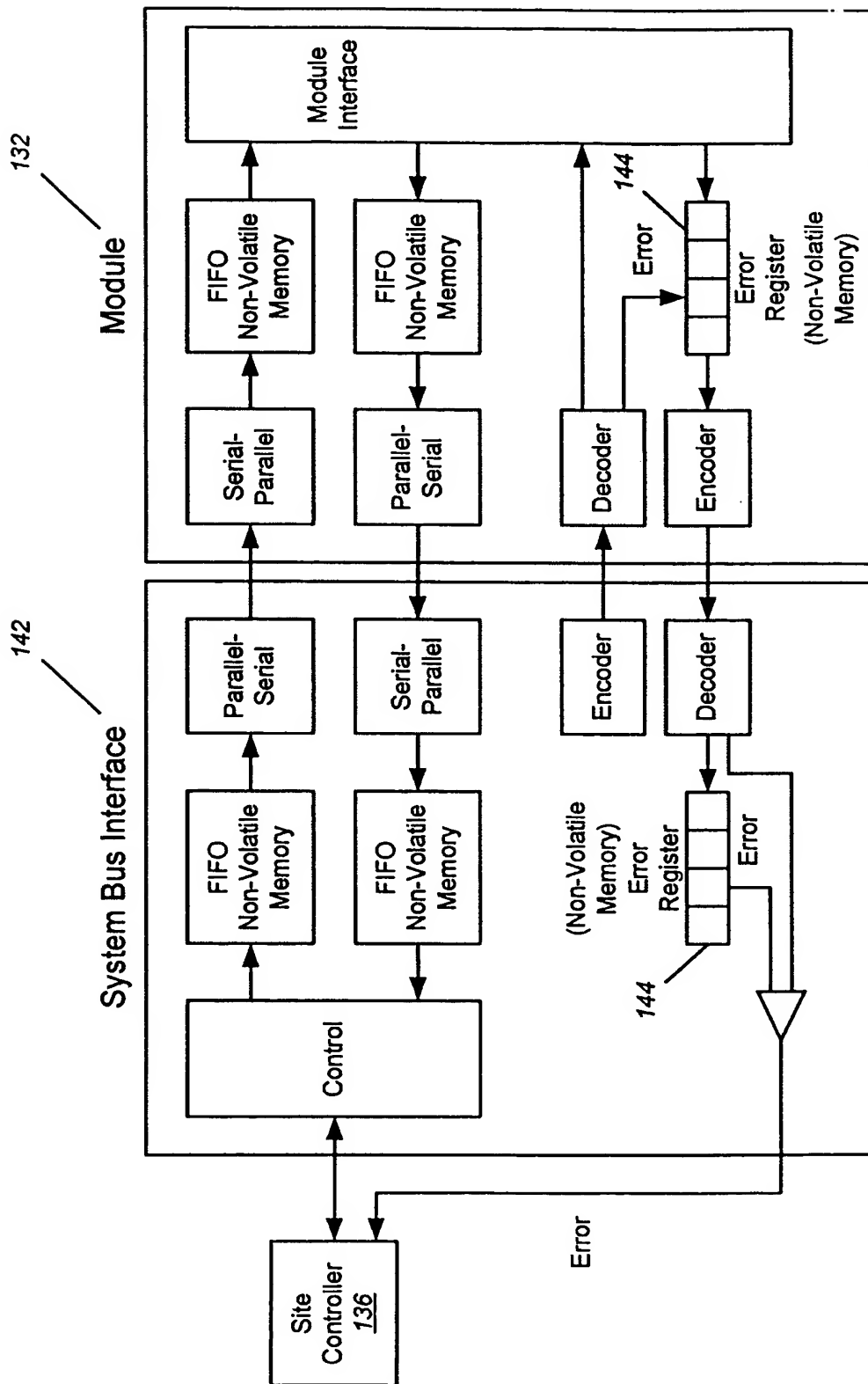


FIG. 14